

04/02/02

2818



First Named Inventor	Roohparvar	<b>TRANSMITTAL FORM UNDER 37 CFR 1.10 (LARGE ENTITY)</b>
Serial No.	09/608,580	
Filing Date	June 30, 2000	
Group Art Unit	2818	
Examiner Name	Trong Q. Phan	
Attorney Docket No.	400.006US01	
Title: ZERO LATENCY-ZERO TURNAROUND SYNCHRONOUS FLASH MEMORY		

Commissioner for Patents  
BOX NON-FEE AMENDMENT  
Washington, D.C. 20231

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Enclosures							
The following documents are enclosed:							
<input checked="" type="checkbox"/>	An Amendment and Response to Office Action (6 pgs., including "marked up" version).						
<input checked="" type="checkbox"/>	Two Sheets of Redlined drawings (Figures 1A and 32)						
<input checked="" type="checkbox"/>	A return postcard.						
Please charge any additional fees or credit any overpayments to Deposit Account No. 501373.							
<b><u>CUSTOMER NUMBER 27073</u></b>							
Calculation							
	Number of Claims		Priv. Amt.	Extra Claims		Fee	Fee Paid
Total Claims		-20			X	\$18	= \$
Independent Claims		-3			X	\$80	= \$
						=	
Total							\$
Submitted By							
Name	Russell D. Slifer	Reg. No.	39,838	Telephone	(612) 312-2202		
Signature				Date	April 1, 2002		
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"Express Mail" mailing label number: EL823842955US Date of Deposit: April 1, 2002							
These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to the Commissioner for Patents, Washington, D.C. 20231.							
(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)							